

	Search Text
1	(layout with vesus with schematic lvs) same width same (wire line interconnect\$5 connect\$5)
2	(layout with vesus with schematic lvs) same width same (wire line interconnect\$5 connect\$5) and "716"/\$.ccls.
3	(check\$4 verif\$6 match\$5 compar\$6) same width same (wire line interconnect\$5 connect\$5) same (layout\$4) same schematic
4	(width and marker and layout and versus and schematic and line).clm.
5	(width and layout and schematic and (line wire)).clm.
6	(check\$4 verif\$6 match\$5 compar\$6) same width same (wire line interconnect\$5 connect\$5) same (layout\$4 or schematic) and (layout with vesus with schematic lvs)
7	width same (wire line interconnect\$5 connect\$5) same (mark\$4 target outline boundar\$4)
8	width same (wire line interconnect\$5 connect\$5) same (mark\$4 target outline boundar\$4) and (check\$4 verif\$6 match\$5 compar\$6) same width same (wire line interconnect\$5 connect\$5) same (layout\$4) same schematic
9	width same (wire line interconnect\$5 connect\$5) same (mark\$4 target outline boundar\$4) and (layout with vesus with schematic lvs)
10	width same (wire line interconnect\$5 connect\$5) same (mark\$4 target outline boundar\$4) and (layout with vesus with schematic lvs) same width near3 (wire line interconnect\$5 connect\$5)
11	width near3 (wire line interconnect\$5 connect\$5) same (error\$4 meet\$4 met rule violat\$4)
12	width near3 (wire line interconnect\$5 connect\$5) same (error\$4 meet\$4 met rule violat\$4) and (layout with vesus with schematic lvs)
13	width same (wire line interconnect\$5 connect\$5) same (mark\$4 target outline boundar\$4) and (layout with vesus with schematic lvs) same width near3 (wire line interconnect\$5 connect\$5) and width near4 (mark\$4 target outline boundar\$4)
14	(width and mark\$ and schematic and line).clm.
15	(error and width and mark\$ and schematic and line).clm.
16	((line wire) same width same (accurac\$4 error) and add\$4 and mark\$4 and layout same versus same schematic and design same line same width same versus same layout).clm.
17	((line wire) same width same (accurac\$4 error) and add\$4 and mark\$4 and layout same versus same schematic and design same line same width same versus same layout).clm.
18	christophe near3 chevallier
19	christophe near3 chevallier and (width same marker same (wire line) and schematic and layout\$4 same (creat\$4 generat\$4) same width and (lvs layout with versus with schematic)).clm.